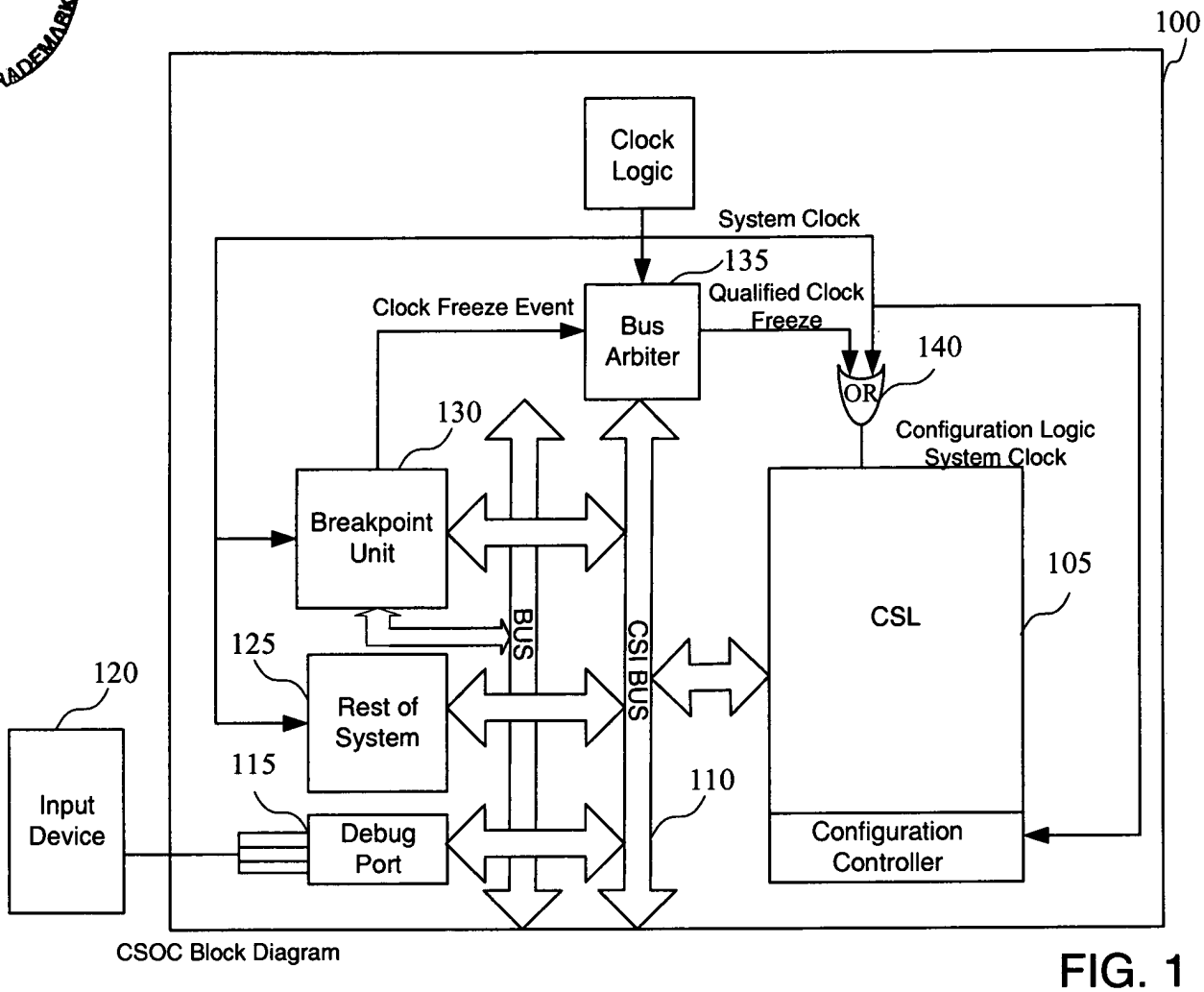
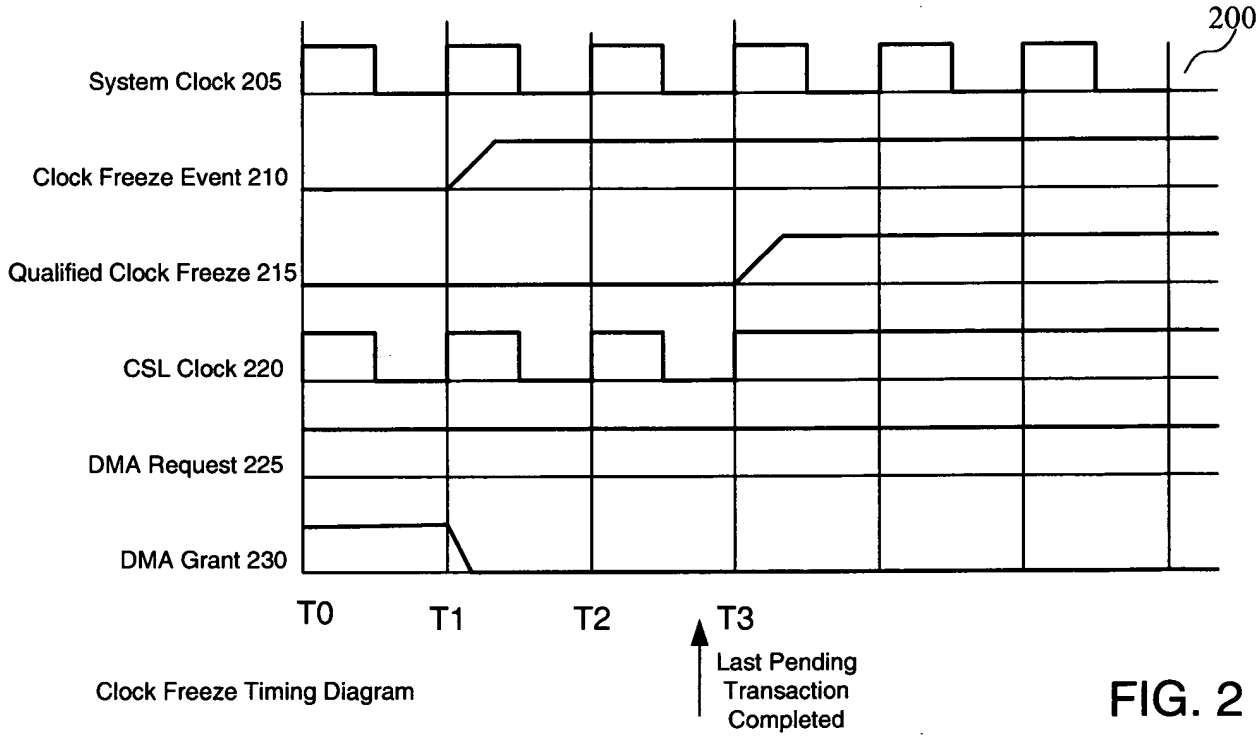


CSOC Block Diagram

FIG. 1



Clock Freeze Timing Diagram

FIG. 2